

WEST Search History

DATE: Monday, October 04, 2004

Hide?	Set Name	Query	Hit Count
<i>DB=JPAB; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L19	PLATE adj3 OVERLAP\$ adj4 gate	0
<input type="checkbox"/>	L18	CELL PLATE adj3 OVERLAP\$	2
<input type="checkbox"/>	L17	CELL PLATE OVERLAP\$	0
<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L16	L2 AND CELL PLATE OVERLAP\$	1
<input type="checkbox"/>	L15	L3 AND CELL PLATE OVERLAP\$	1
<input type="checkbox"/>	L14	L5 AND (CELL PLATE) ADJ5 OVERLAP\$ ADJ5 GATE	2
<input type="checkbox"/>	L13	L5 AND (CELL PLATE) ADJ3 OVERLAP\$ ADJ5 GATE	0
<input type="checkbox"/>	L12	L10 AND (CELL PLATE) ADJ3 OVERLAP\$ ADJ5 GATE	0
<input type="checkbox"/>	L11	L9 AND (MINIMIZ\$ OR REDUC\$) ADJ5 (SIZE OR SPACE OR AREA)	36
<input type="checkbox"/>	L10	L9 AND (MINIMIZ\$ OR REDUC\$)	62
<input type="checkbox"/>	L9	L7 AND CELL PLATE ADJ5 GATE	64
<input type="checkbox"/>	L8	L7 AND CELL PLATE OVERLAP\$	0
<input type="checkbox"/>	L7	L6 AND SUBSTRATE	1602
<input type="checkbox"/>	L6	L5 AND CELL PLATE	1721
<input type="checkbox"/>	L5	L4 AND CELL\$	29697
<input type="checkbox"/>	L4	L3 AND OXIDE	39589
<input type="checkbox"/>	L3	L2 AND MEMORY	89258
<input type="checkbox"/>	L2	L1 AND GATE	199976
<input type="checkbox"/>	L1	TRANSISTOR	317397

END OF SEARCH HISTORY

VIET Q. NGUYEN
PRIMARY EXAMINER